

# 500ADA-W

## Word Clock Distribution Amplifier (1x9)

The 500ADA-W is a SDIF-2 Word Clock distribution amplifier. The input can be configured to be high impedance or terminated to 75Ω. The 500ADA-W provides continuous voltage gain adjust from -6dB to +6dB. The module supports a maximum output signal of 5V.

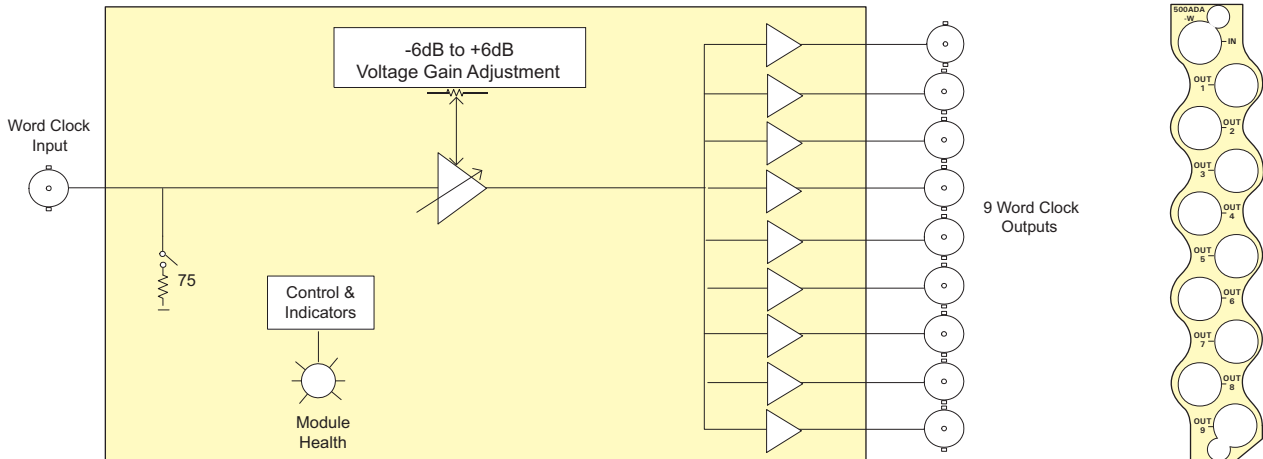
The 500ADA-W is housed in the 3RU 500FR **exponent** frame that will hold up to 16 modules.

### Features & Benefits

- Jumper selectable 75Ω or high impedance (1kΩ typical) input
- DA has voltage gain adjustment range from -6dB to +6dB
- Outputs can drive into 75Ω loads

### Card Edge LEDs

- Module status/Local Fault
- Power supply status



### Specifications

#### Word Clock Input:

Standard: SDIF-2 Word Clock  
Number of Inputs: 1  
Connector: BNC per IEC 61169-8 Annex A  
Level: 0 to 5V (terminated or unterminated)  
Impedance: Selectable 75Ω or high impedance (1kΩ typical)

#### Word Clock Outputs:

Number of outputs: 9  
Connector: BNC per IEC 61169-8 Annex A  
Output impedance: 75Ω  
Max. Output levels: 5V into 75Ω load  
10V into high impedance load  
Min. Output Level: 0V  
Voltage Gain Range: -6dB to +6dB  
Frequency range: 28 kHz - 100 kHz

#### Electrical:

Voltage: +12V DC  
Power: 1.2W  
EMI/RFI: Complies with FCC Part 15 Class A, EU EMC Directive

#### Physical:

Number of Slots: 1

### Ordering Information

500ADA-W Word Clock Distribution Amplifier (1x9)

Enclosures:  
500FR  
S501FR

**exponent**  
Compact High Density Distribution Frame  
Standalone enclosure